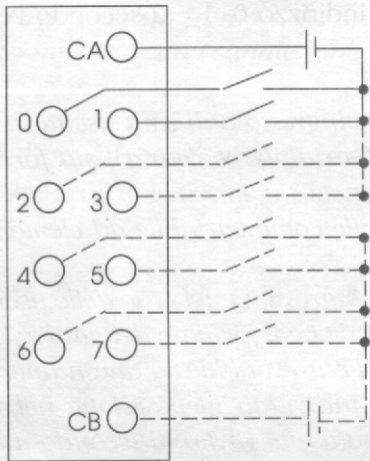
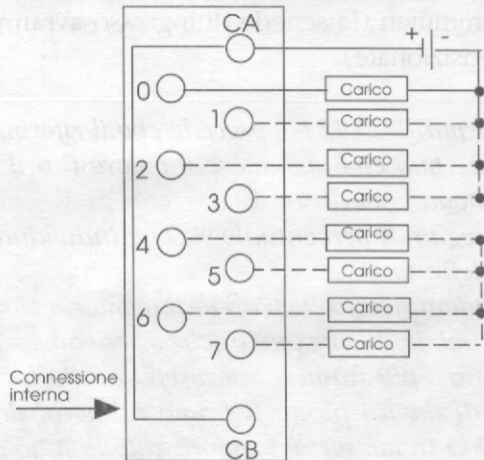


SLOT 00



1.2

SLOT 01

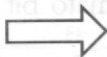


2.2

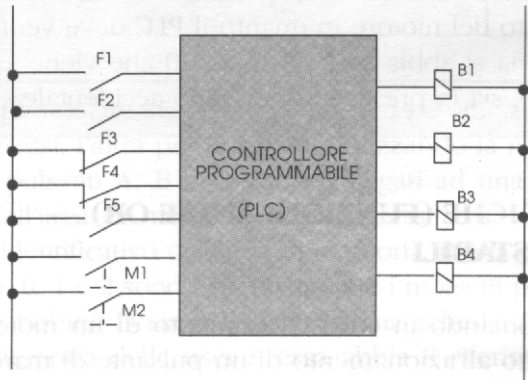
NUMERO DEL BIT



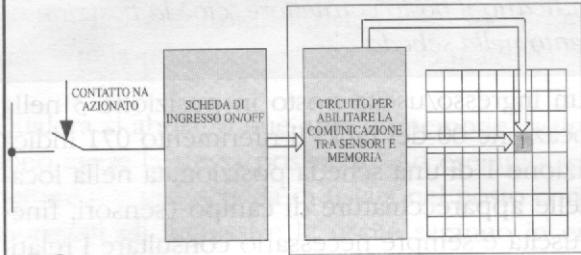
INDIRIZZO DEL BYTE



	MSB							LSB
	7	6	5	4	3	2	1	0
I 0								
I 1								
I 2								
I 3				■				
I 4								
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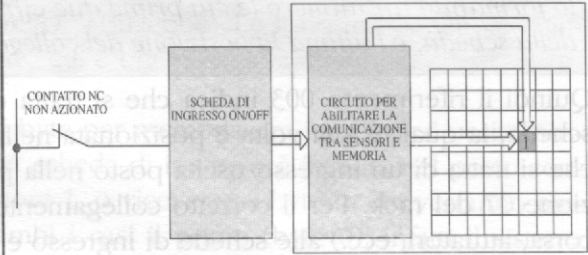


PLC



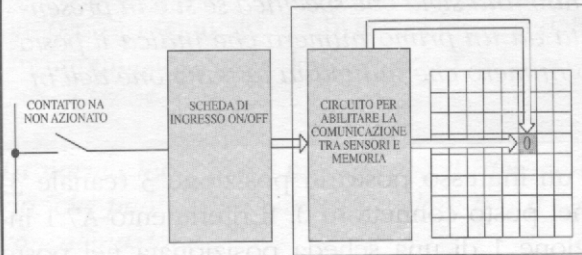
5.2

PLC



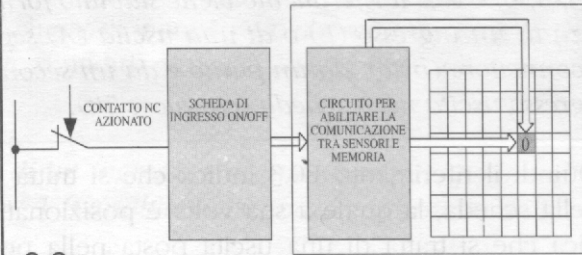
6.2

PLC

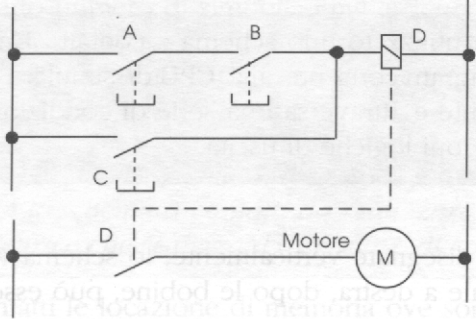


7.2

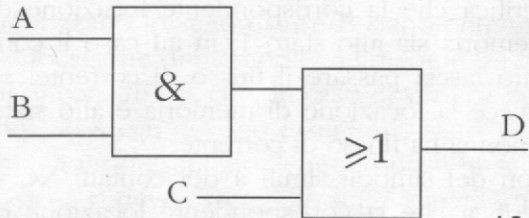
PLC



8.2

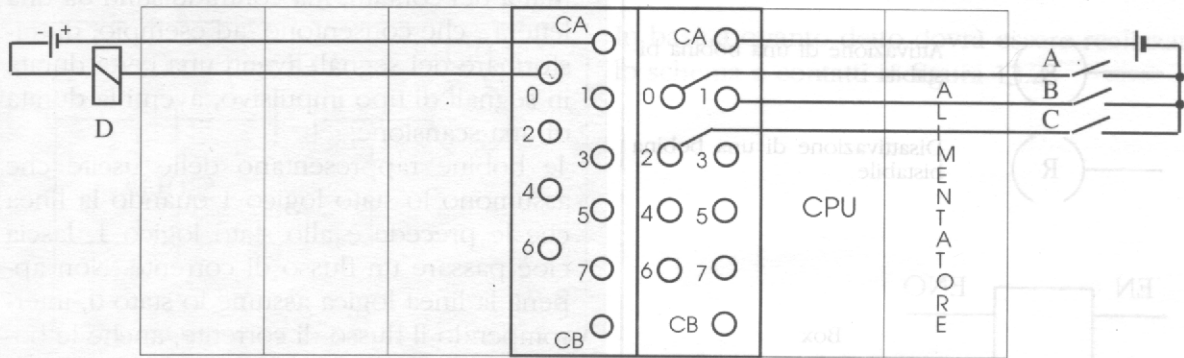


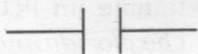
9.2



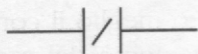
10.2

SLOT 04   SLOT 03   SLOT 02   SLOT 01   SLOT 00

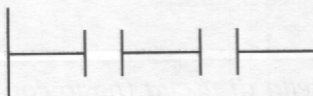




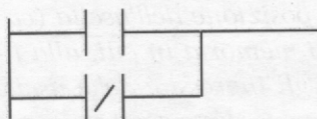
Verifica che la locazione di memoria sia allo stato 1 lasciando passare in tal caso un flusso di corrente



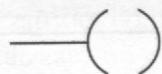
Verifica che la locazione di memoria sia allo stato 0 lasciando passare in tal caso un flusso di corrente



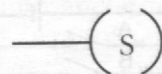
Collegamento in serie = funzione AND



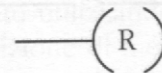
Collegamento in parallelo = funzione OR



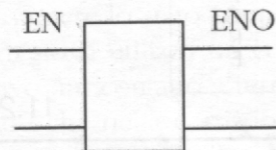
Bobine monostabili



Attivazione di una bobina bistabile

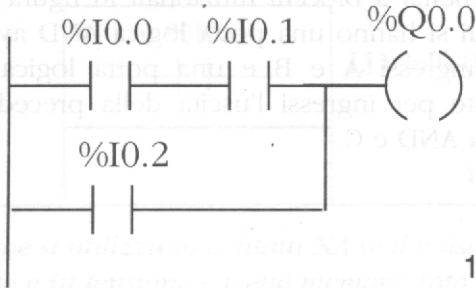


Disattivazione di una bobina bistabile

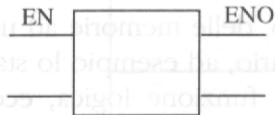
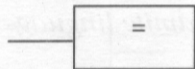
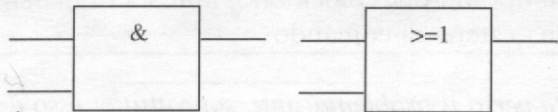


Box

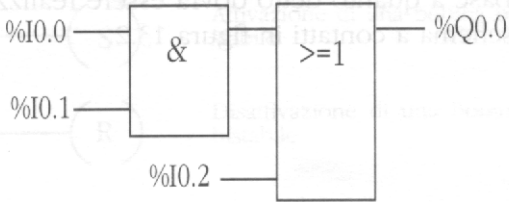




13.2



14.2



15.2

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16.2

%I0.0



LD %I0.0

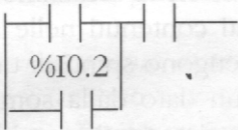
17.2

%I0.0 %I0.1



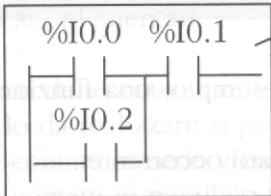
AND %I0.1

%IO.0    %IO.1



OR %IO.2

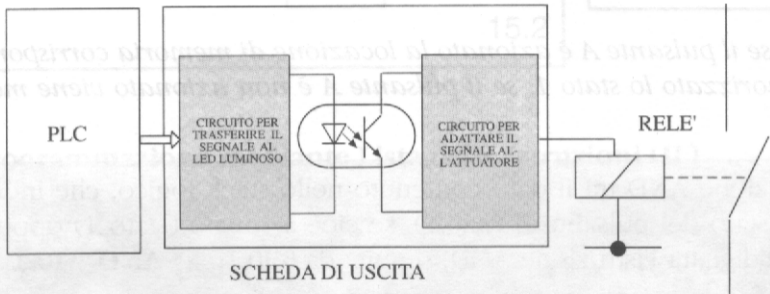
19.2

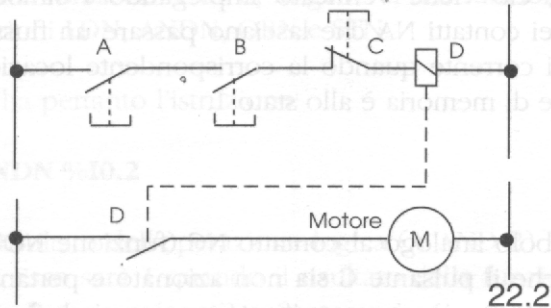


ST %Q0.0

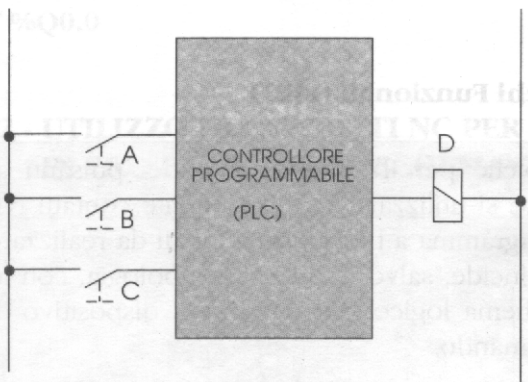
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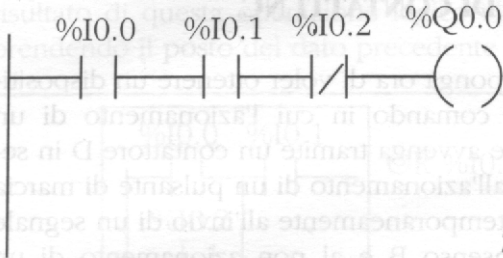


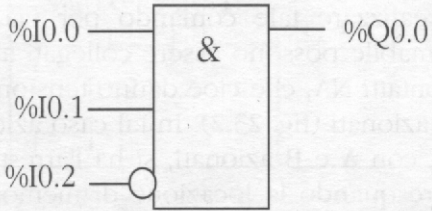


22.2

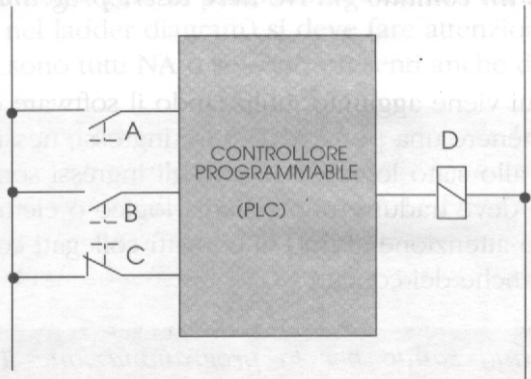


23.2





25.2



%I0.0

%I0.1

%I0.2

%Q0.0

( )

27.2

27.2

%I0.0



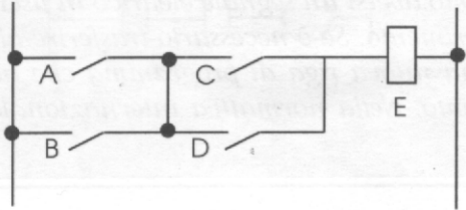
%Q0.0

%I0.1



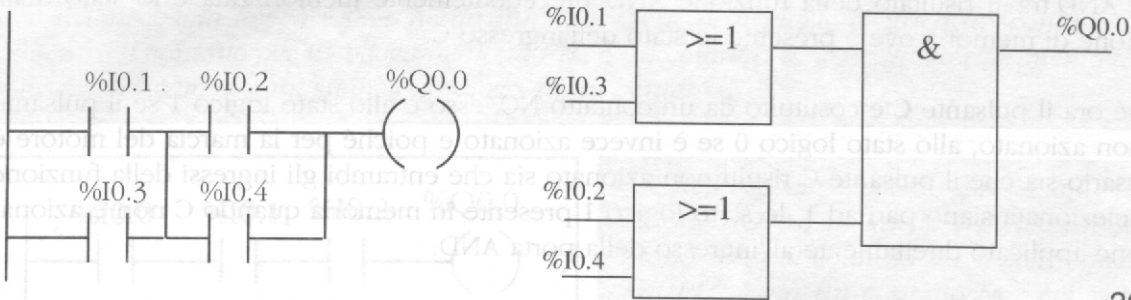
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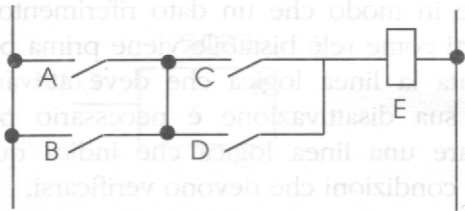




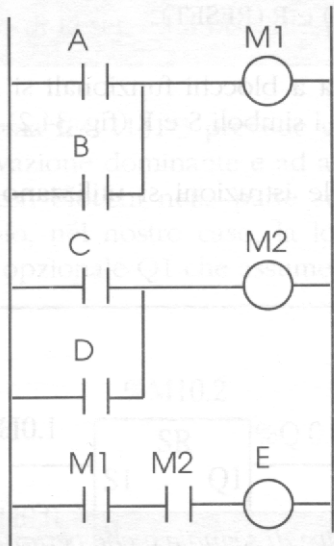
29.2

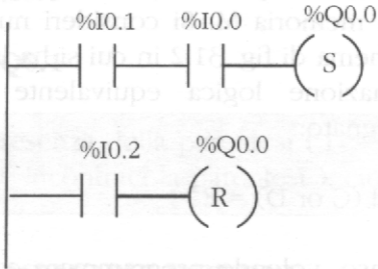


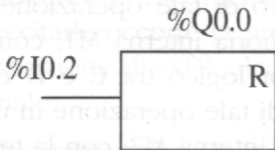
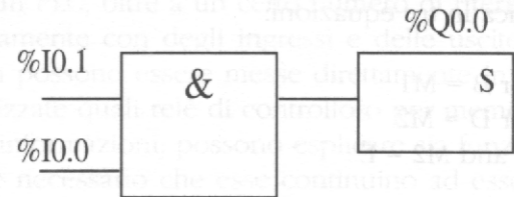


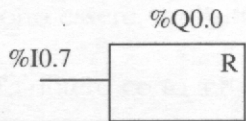
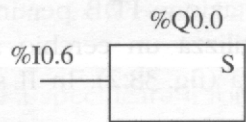
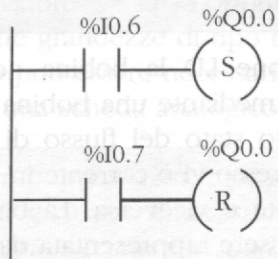


31.2

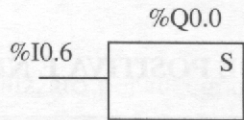
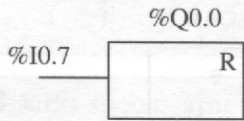
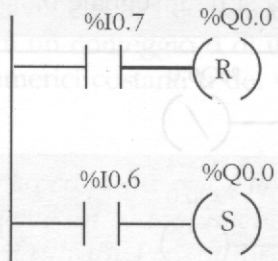




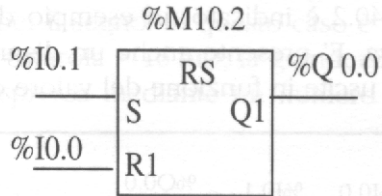
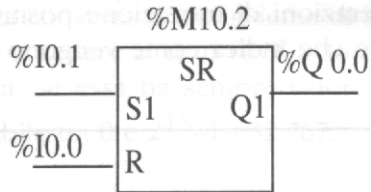


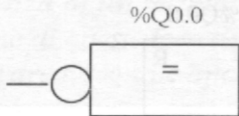
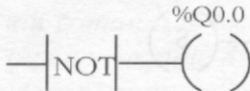
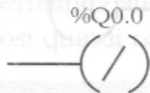


35.2



36.2







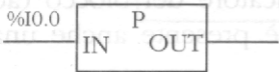
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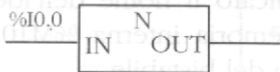
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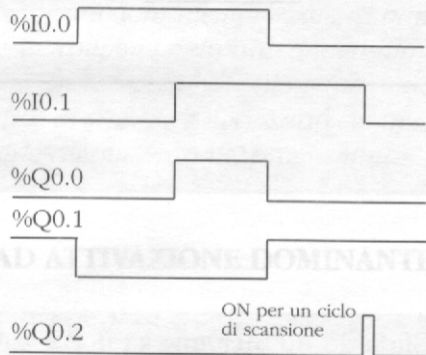
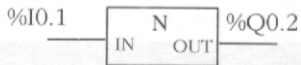
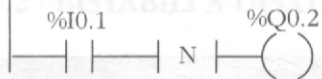
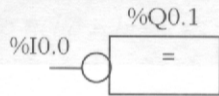
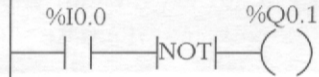
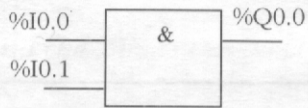
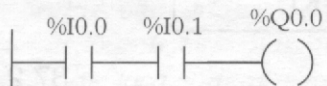


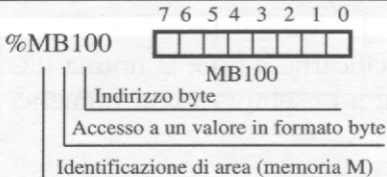
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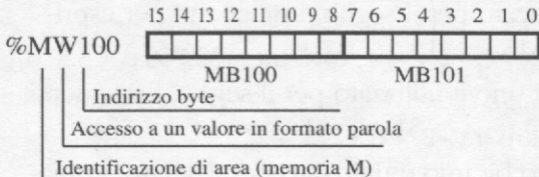
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41.2



42.2

31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0

%MD100



MB100

MB101

MB102

MB103

Indirizzo byte

Accesso a un valore in formato doppia parola

Identificazione di area (memoria M)

63 62 61 60 59 58 57 56 55 54 53 52 51 50 49 48 47 46 45 44 43 42 41 40 39 38 37 36 35 34 33 32 31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0



MB100

MB101

MB102

MB103

MB104

MB105

MB106

MB107

%ML100

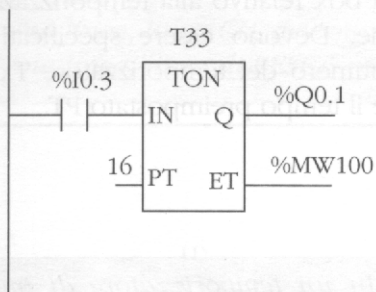
Indirizzo byte

Accesso a un valore in formato lunga parola

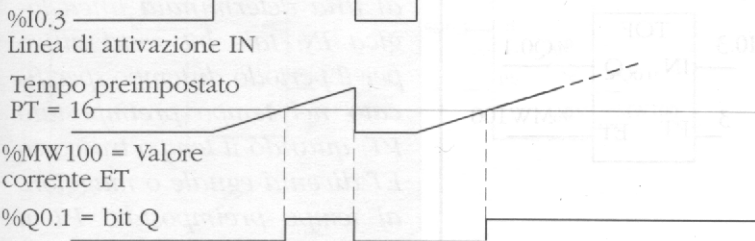
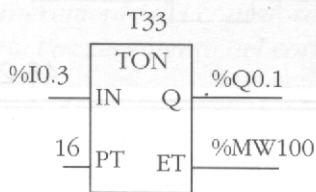
Identificazione di area (memoria M)

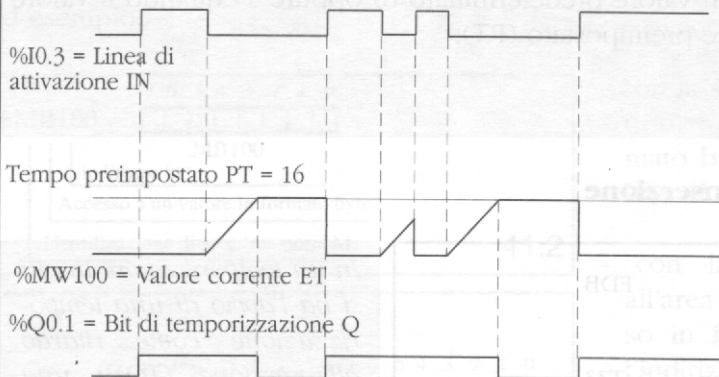
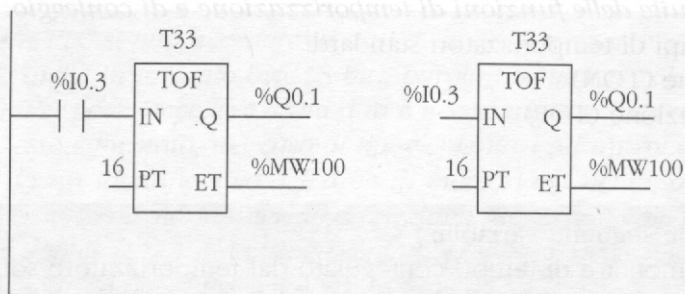
44.2

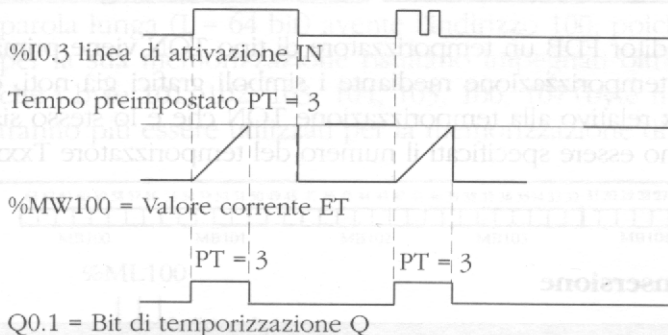
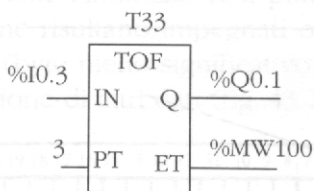
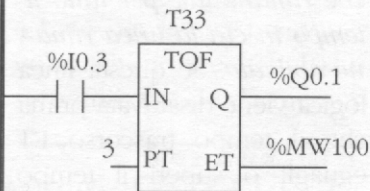
LD



FDB



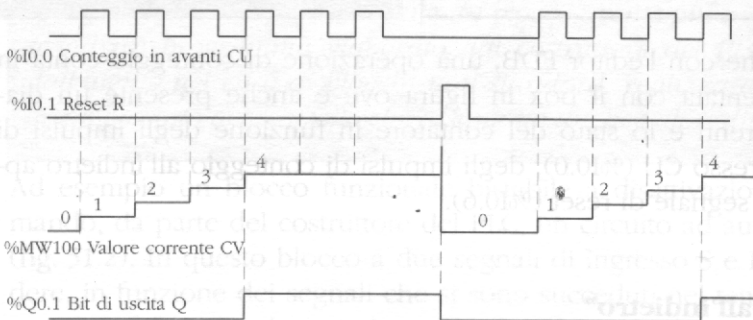
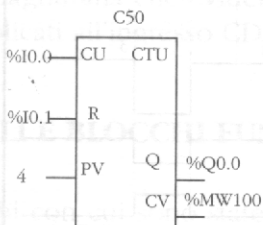
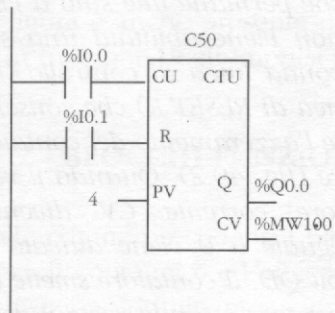






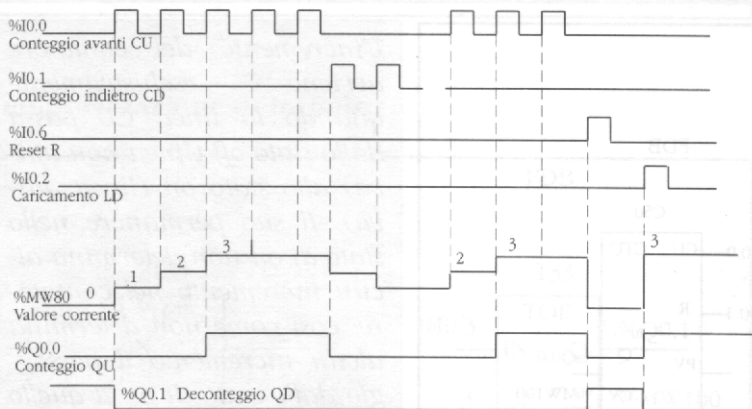
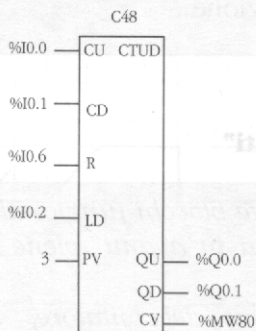
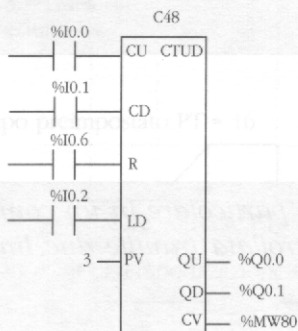
LD

FDB



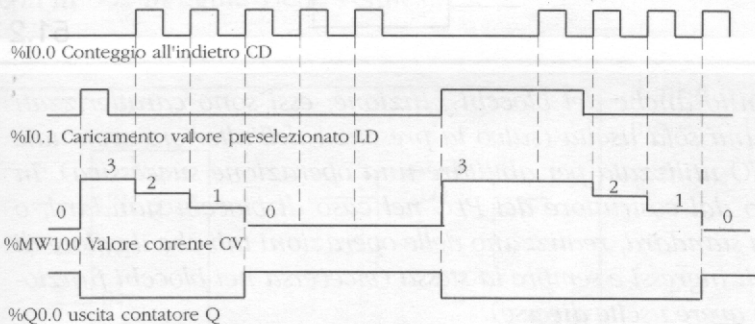
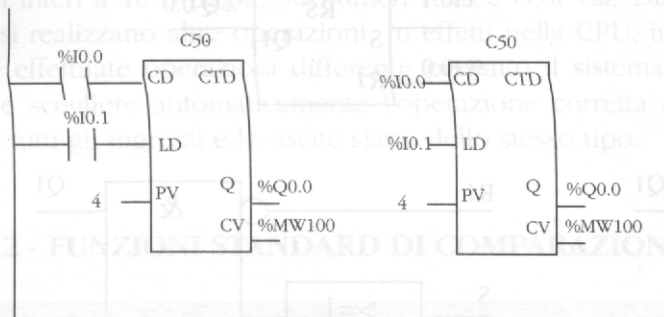
LD

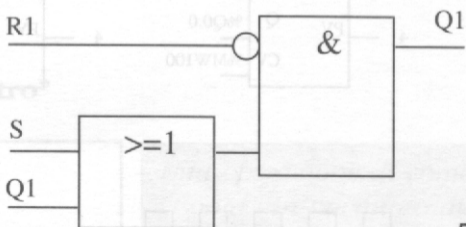
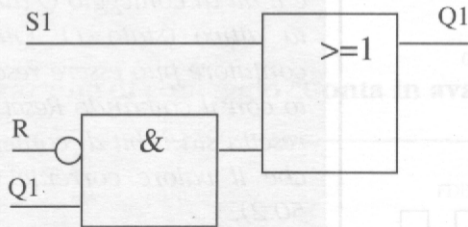
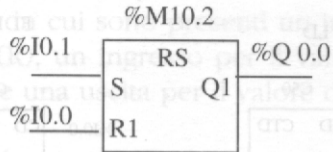
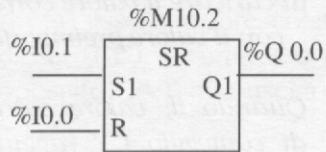
FDB

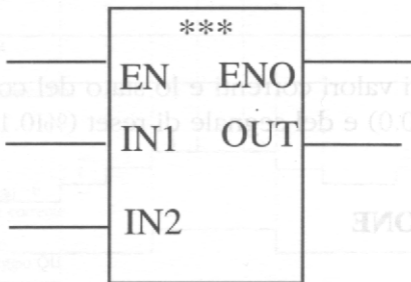


LD

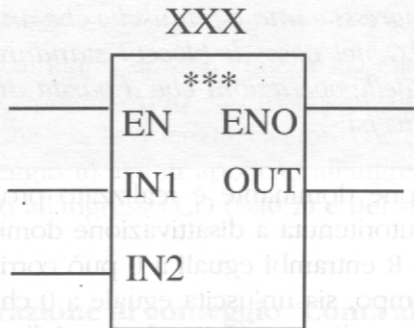
FDB







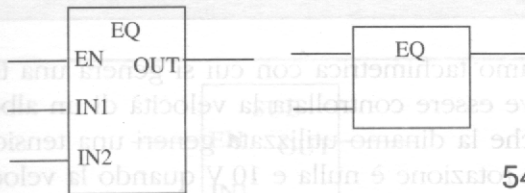
52.2



53.2

LD

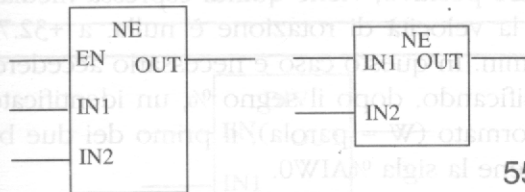
FDB



54.2

LD

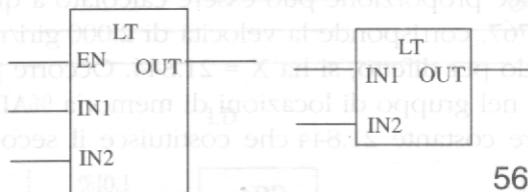
FDB



55.2

LD

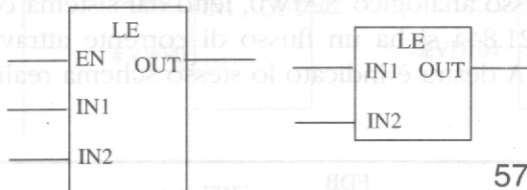
FDB



56.2

LD

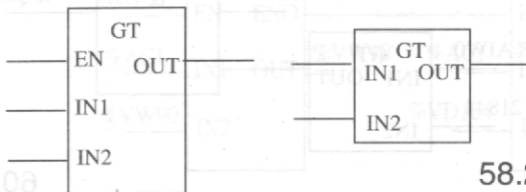
FDB



57.2

LD

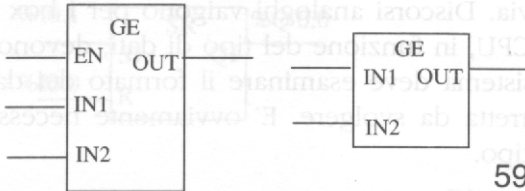
FDB



58.2

LD

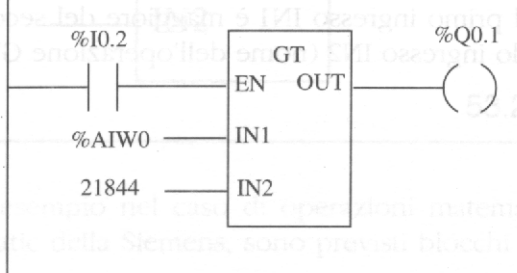
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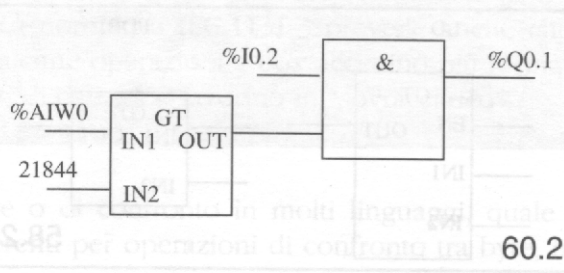
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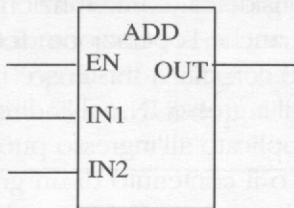


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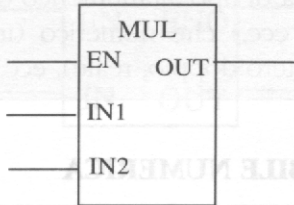


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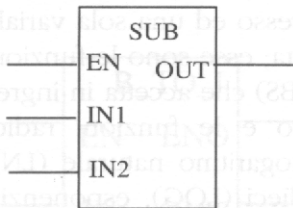




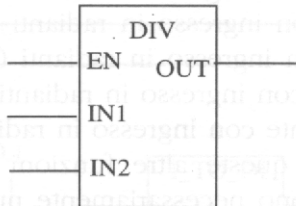
61.2



62.2

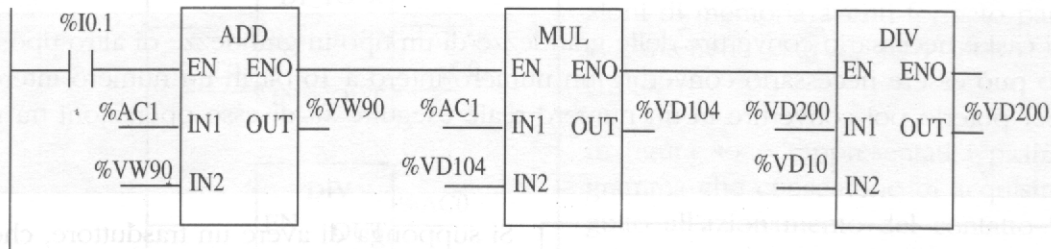


63.2

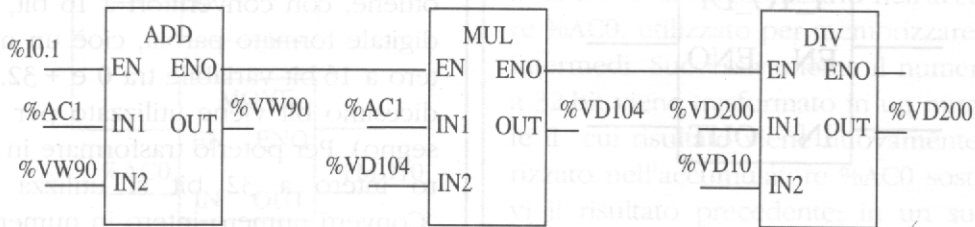


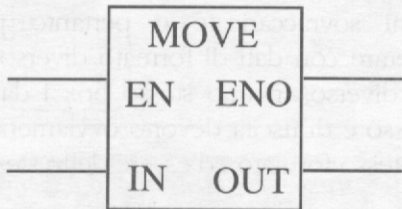
64.2

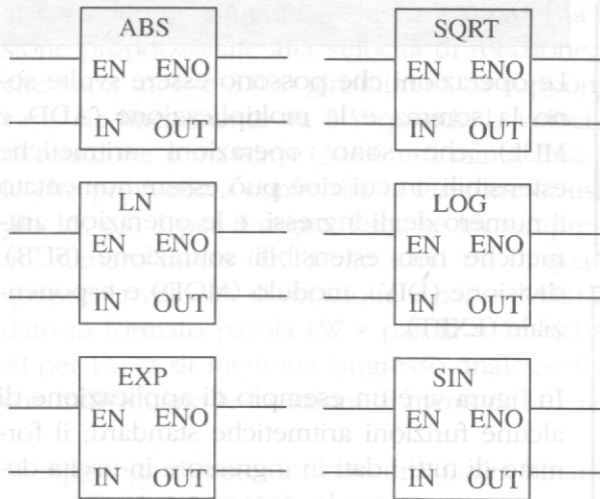
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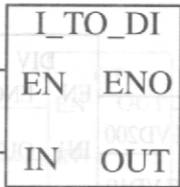


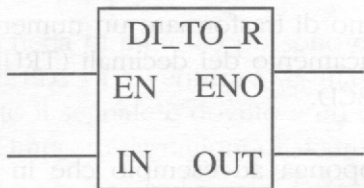
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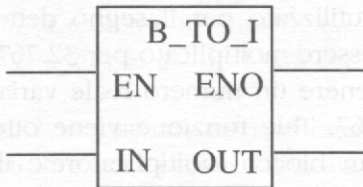








69.2



70.2



